

Electronic Acknowledgement Receipt

EFS ID:	2521054
Application Number:	10803792
International Application Number:	
Confirmation Number:	5596
Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A NUMBER OF DATA OUTPUT PINS CAPABLE OF SELECTIVELY PROVIDING OUTPUT SIGNALS AND TEST METHOD THEREOF
First Named Inventor/Applicant Name:	Nam-Jung Her
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Filer Authorized By:	Hosoon Lee
Attorney Docket Number:	4591-363
Receipt Date:	28-NOV-2007
Filing Date:	17-MAR-2004
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Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 1740
RAM confirmation Number	2710
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1		85B.pdf	302537 <small>68181d279622389a561212e830c19b a07121b3d</small>	yes	2
Multipart Description/PDF files in .zip description					
		Document Description	Start	End	
		Post Allowance Communication - Incoming	1	1	
		Issue Fee Payment (PTO-85B)	2	2	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8378 <small>99117228b6e6ae50a202f6e03c19d3f08 e1409923</small>	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			310915		
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